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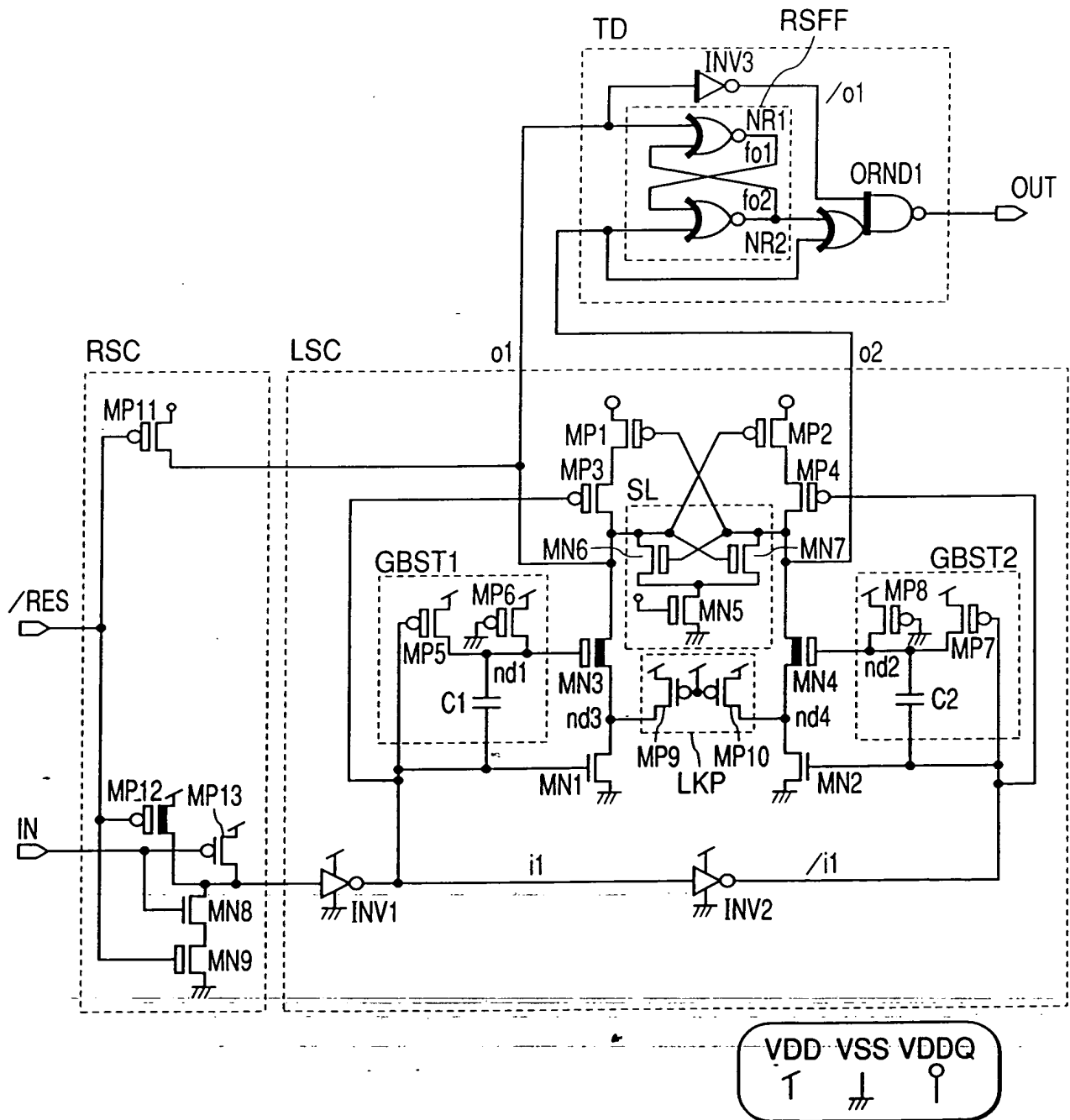
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





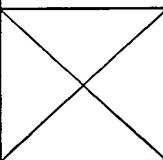
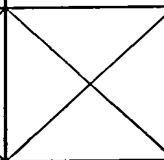
1 / 20

FIG. 1



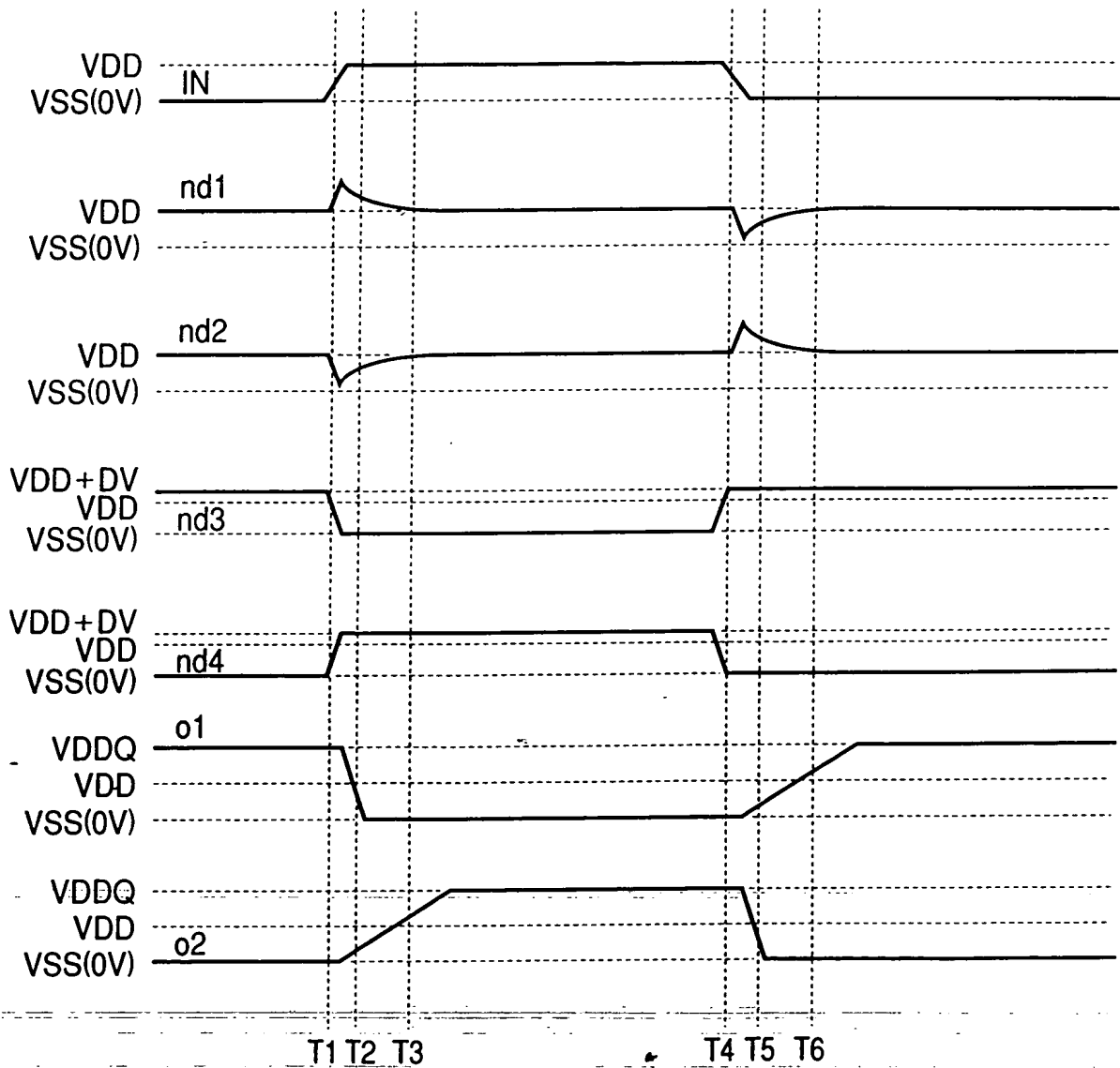
2 / 20

FIG. 2

	PMOS	NMOS	PMOS	NMOS
THICK-FILM MOS (HIGH WITHSTAND VOLTAGE)				
THIN-FILM MOS (LOW WITHSTAND VOLTAGE)				
	STANDARD THRESHOLD MOS		LOW THRESHOLD MOS	

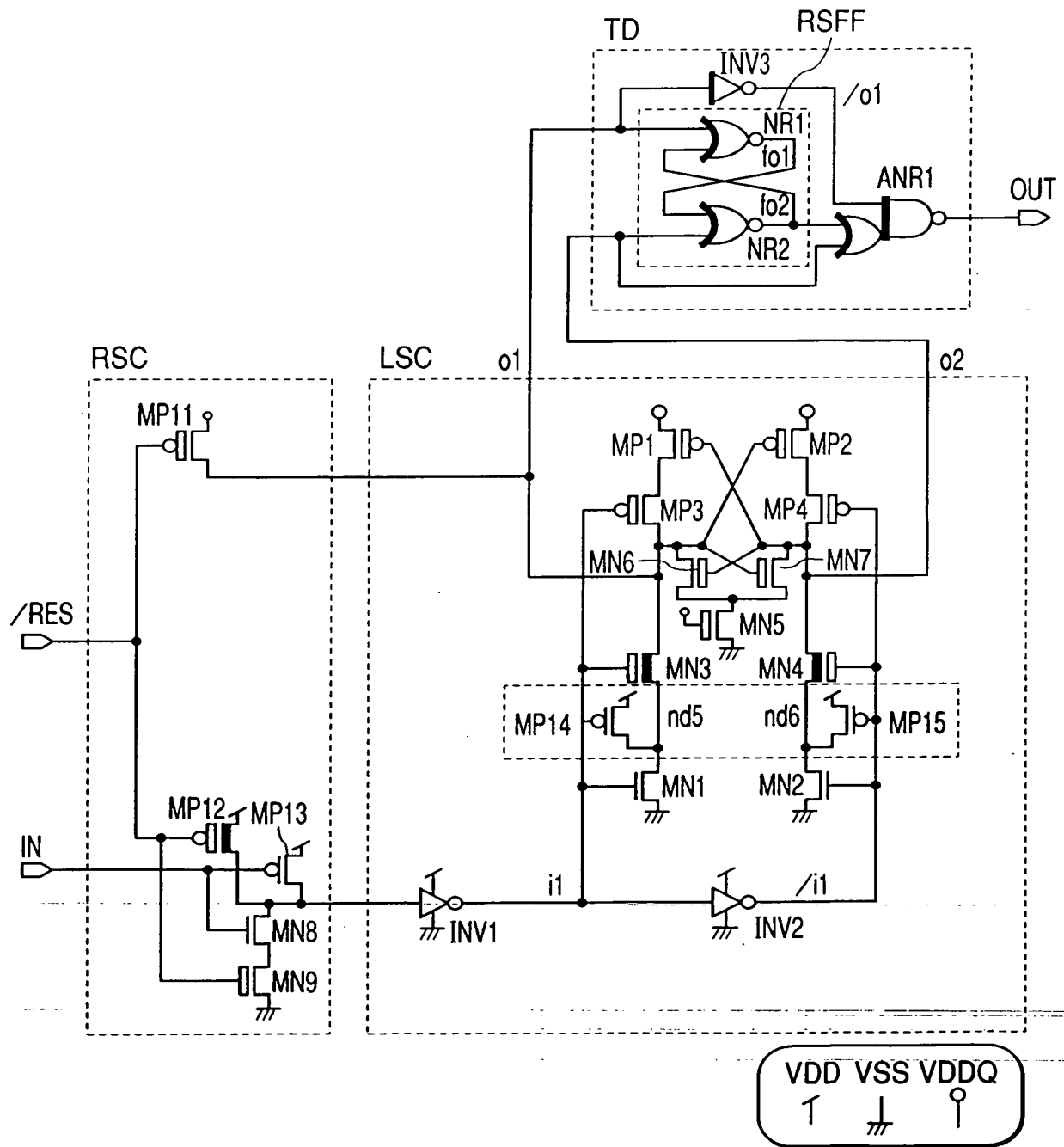
3 / 20

FIG. 3



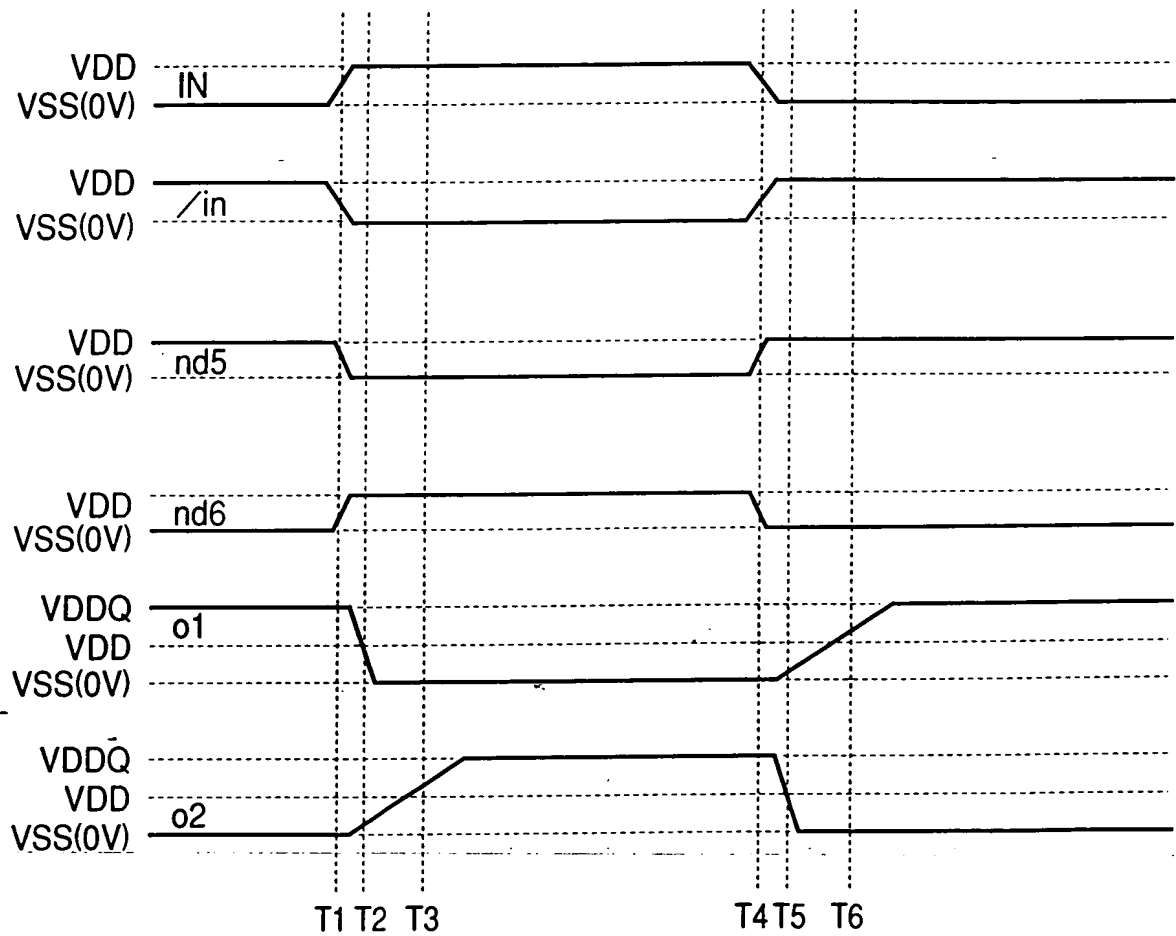
4 / 20

FIG. 4



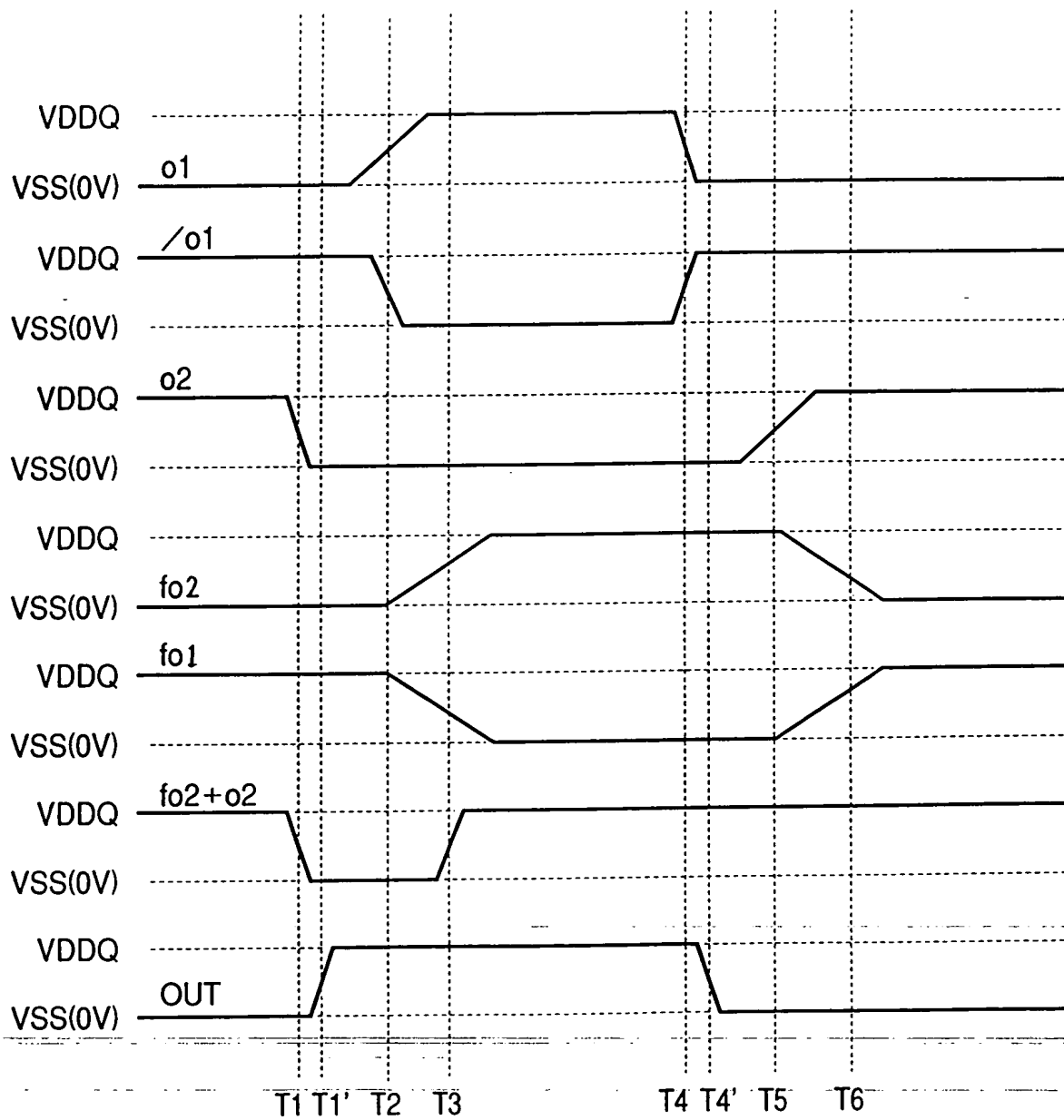
5 / 20

FIG. 5



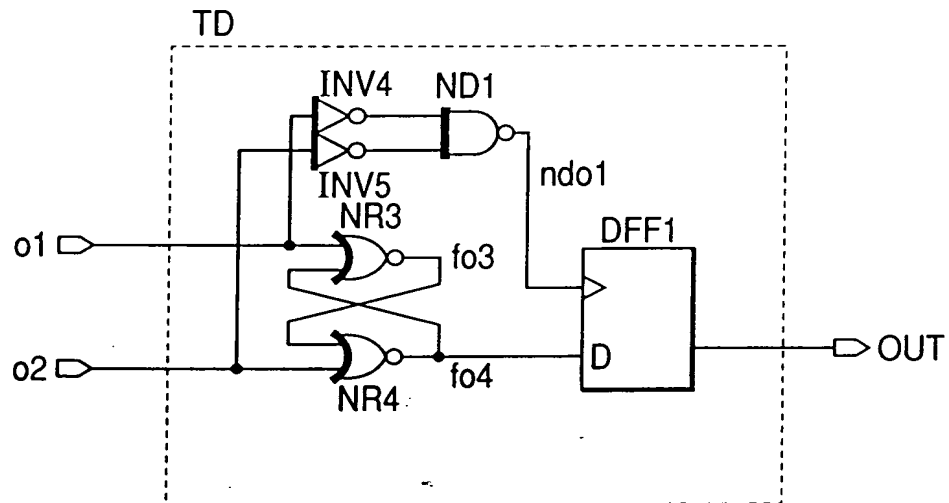
6 / 20

FIG. 6



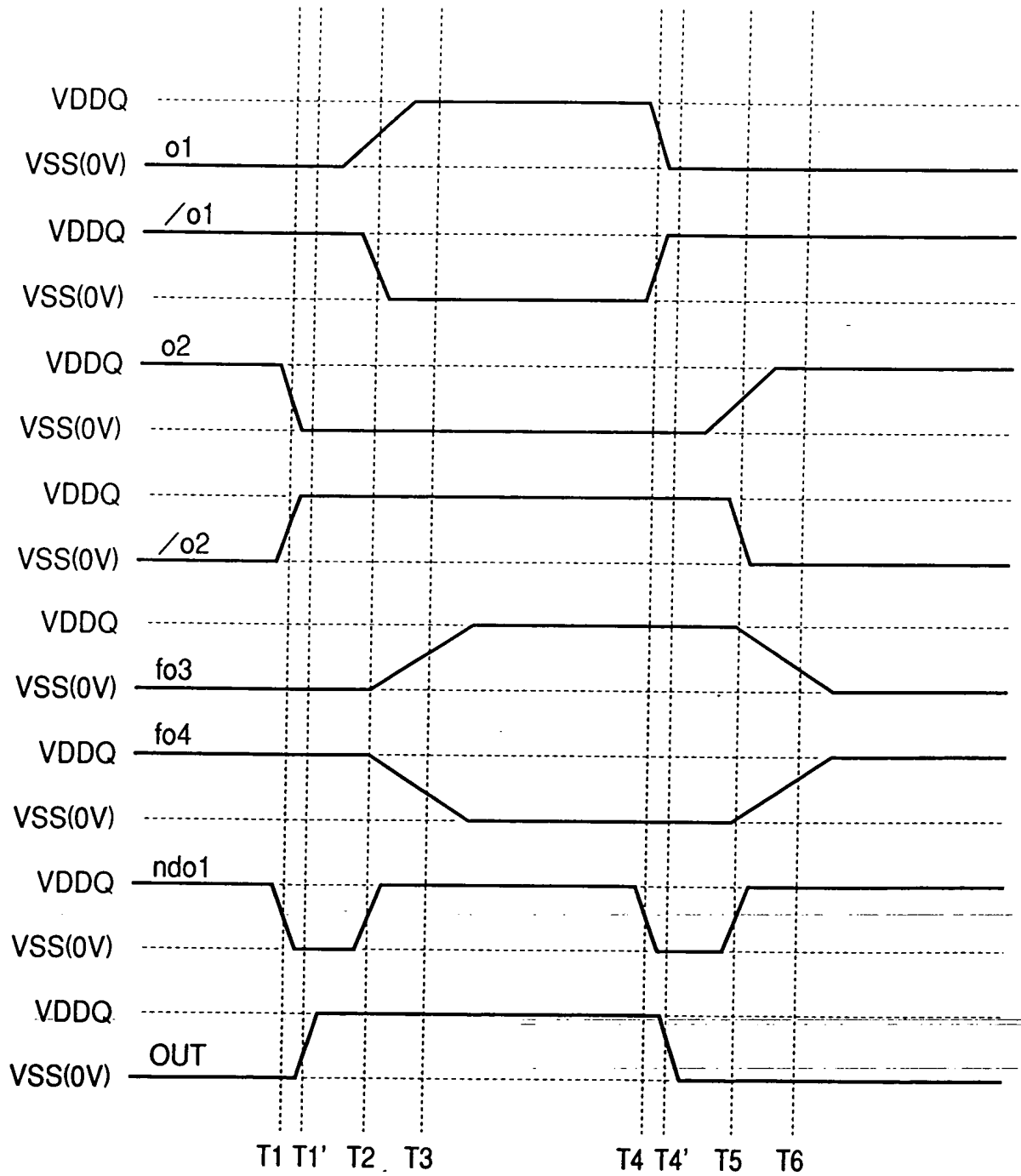
7 / 20

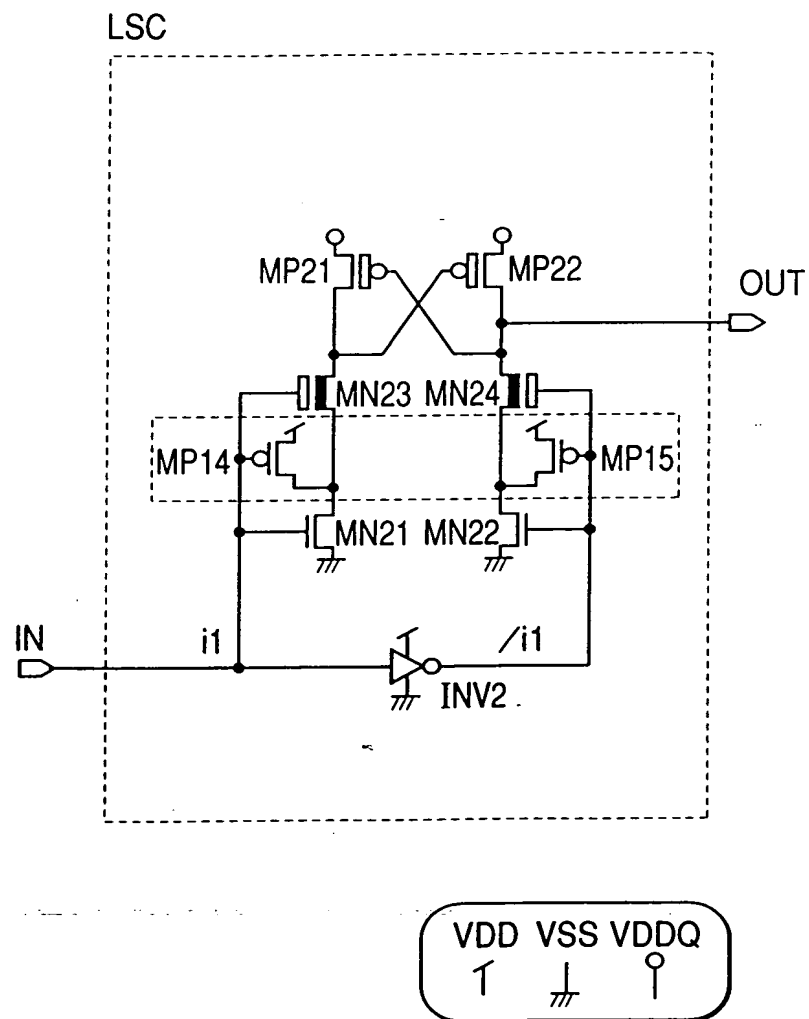
FIG. 7



8 / 20

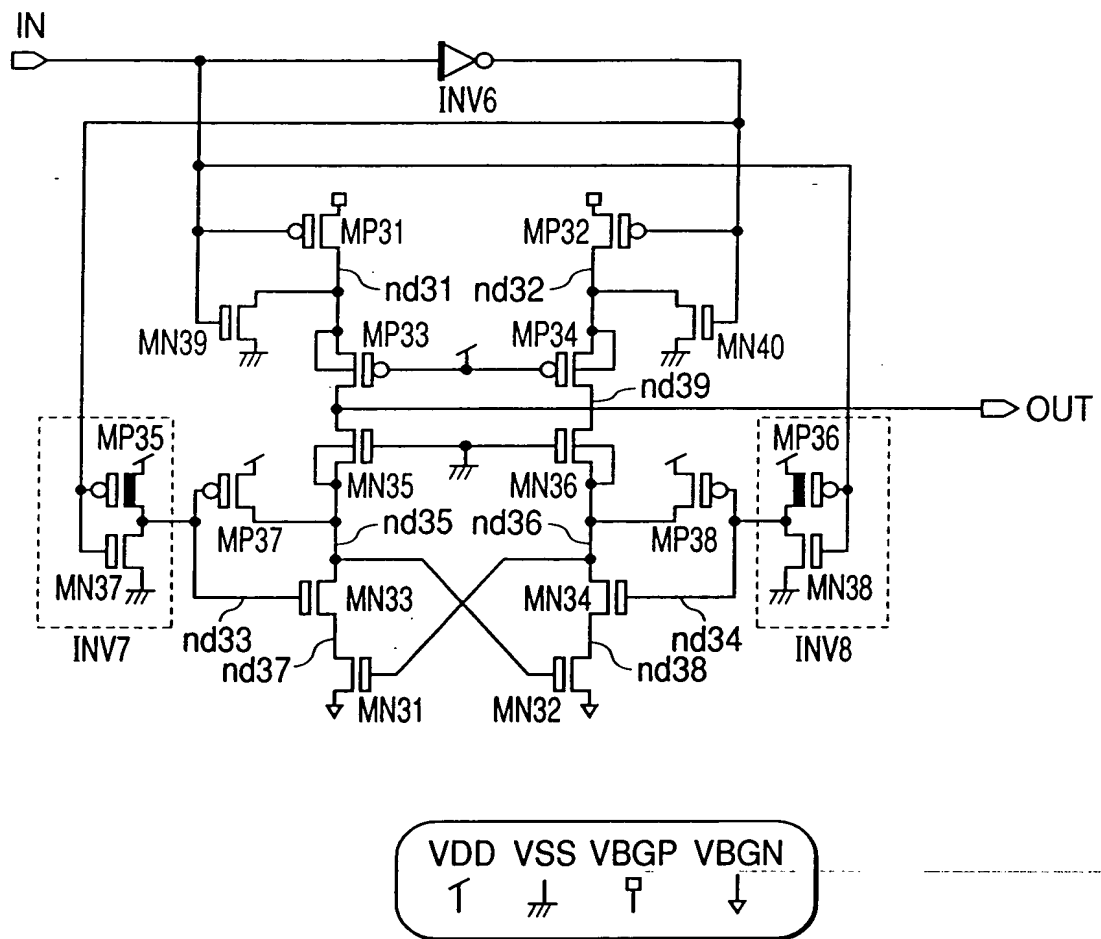
FIG. 8





10 / 20

FIG. 10



11 / 20

FIG. 11

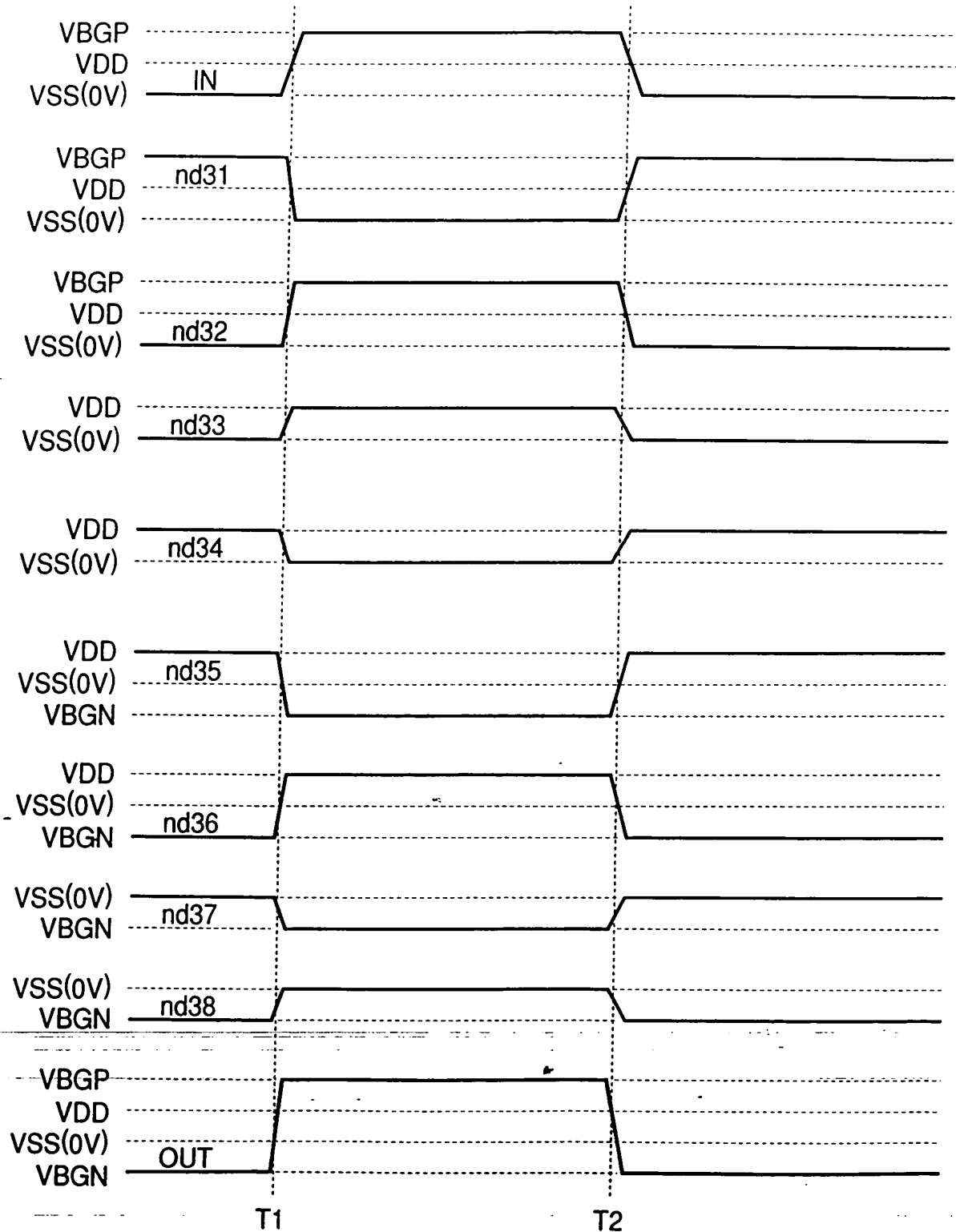
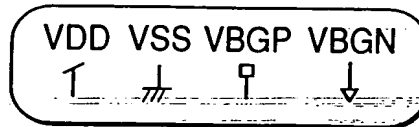
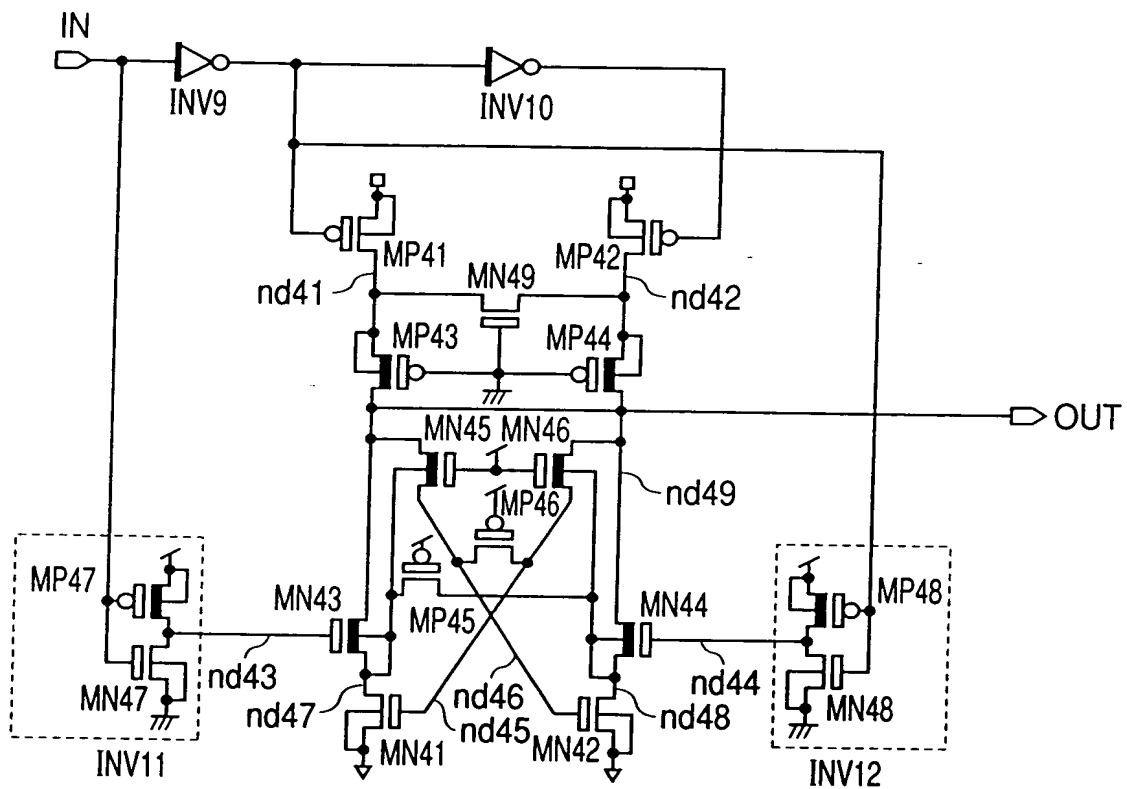
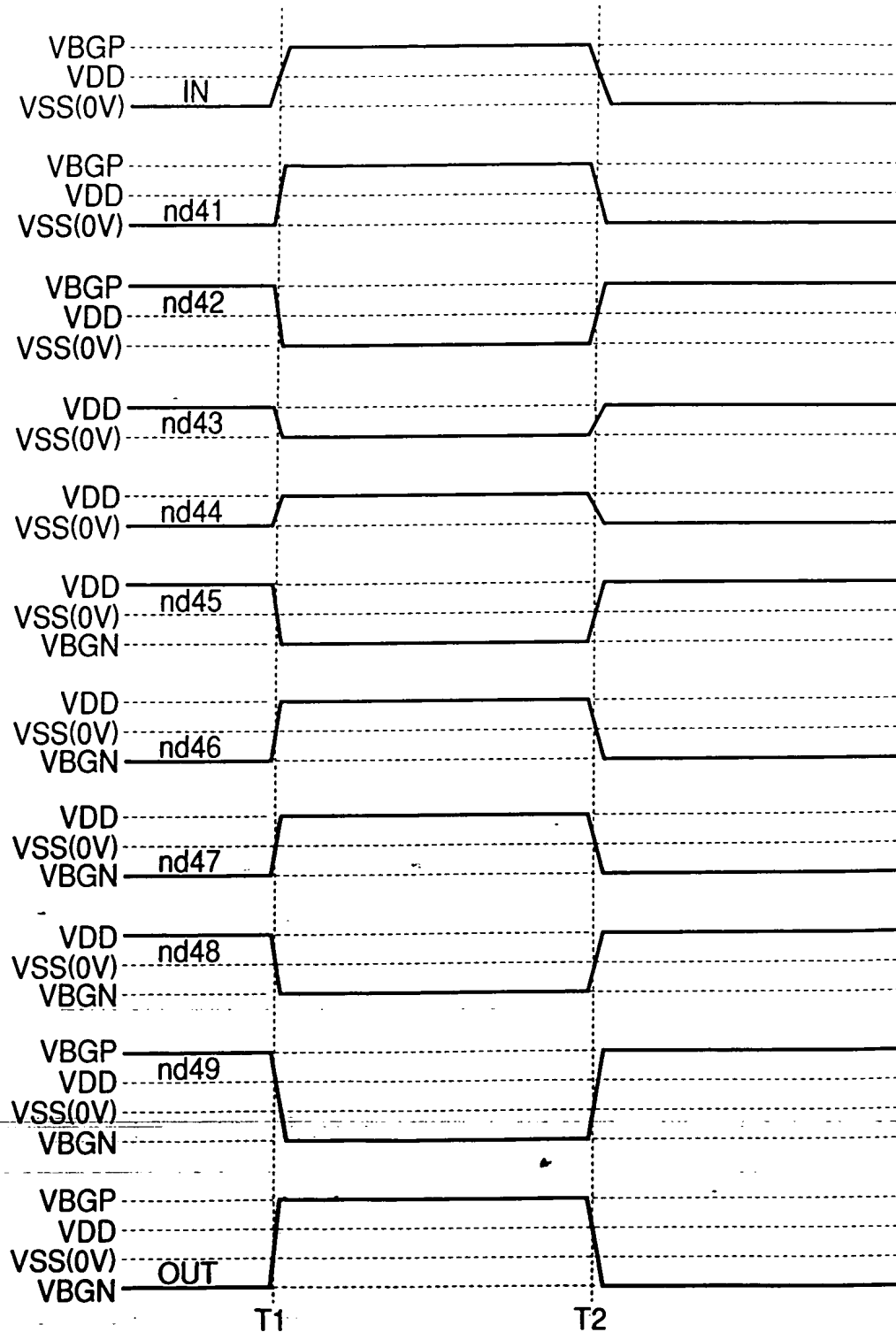


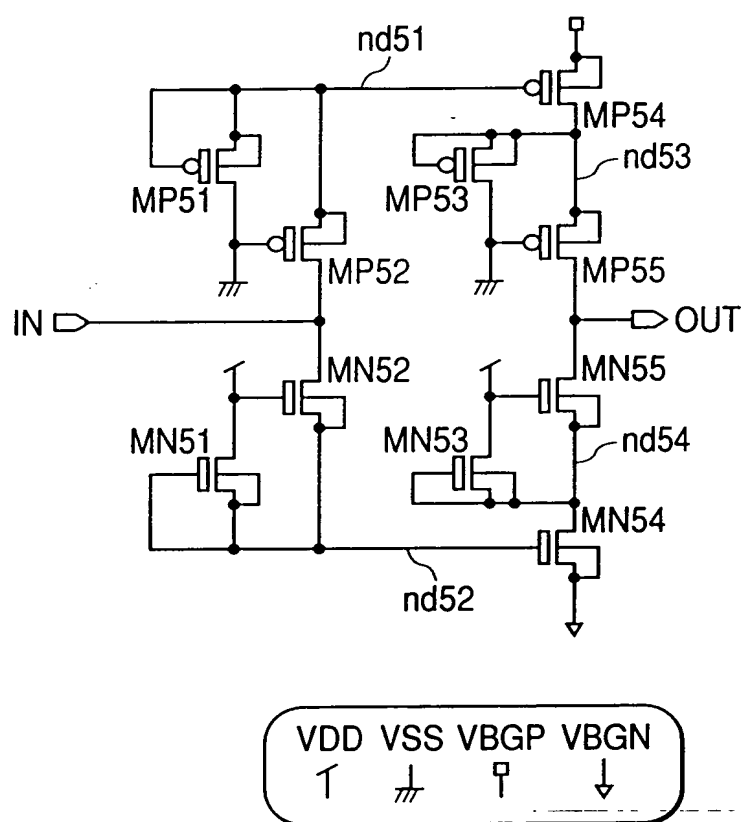
FIG. 12



13 / 20

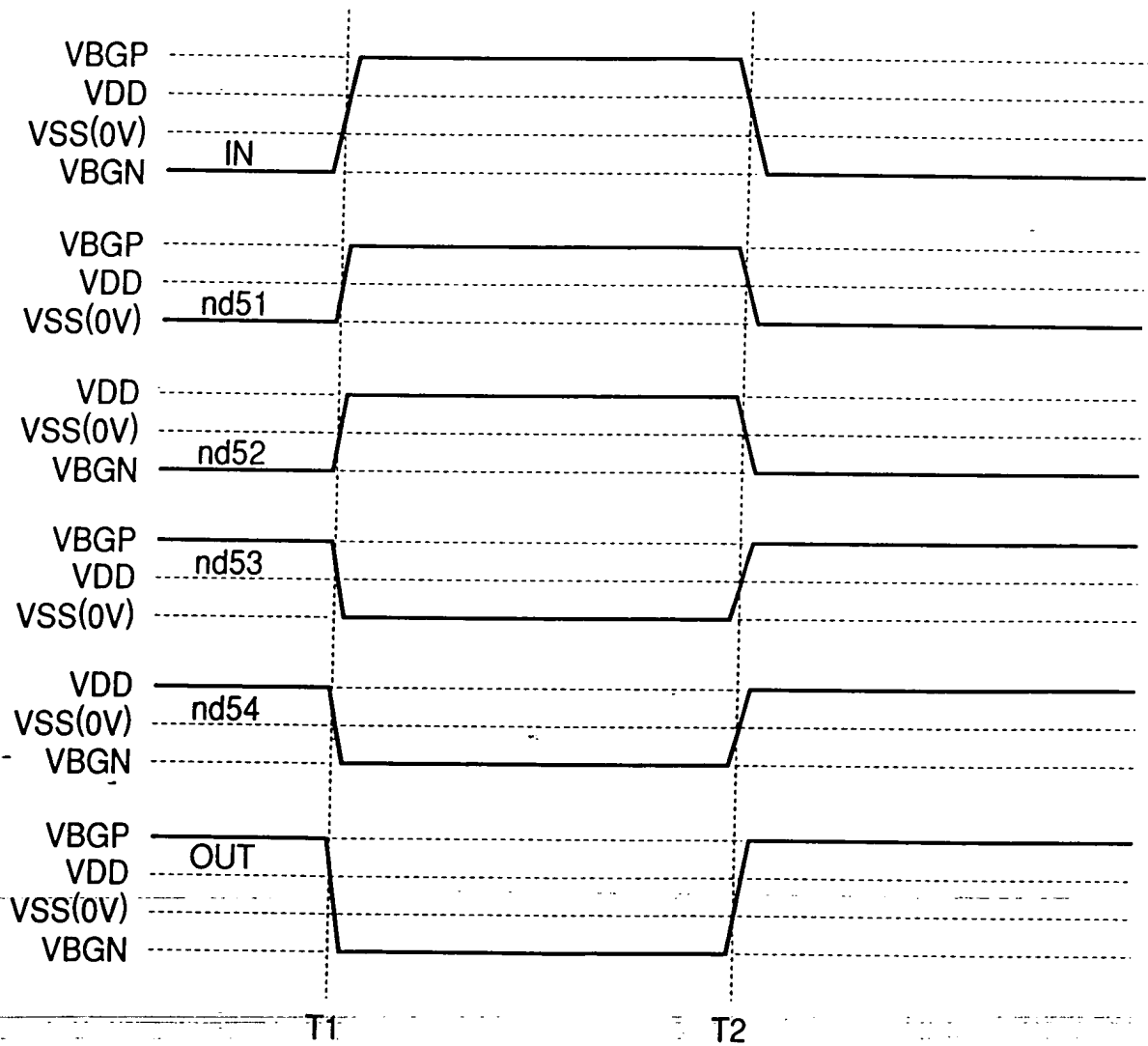
FIG. 13





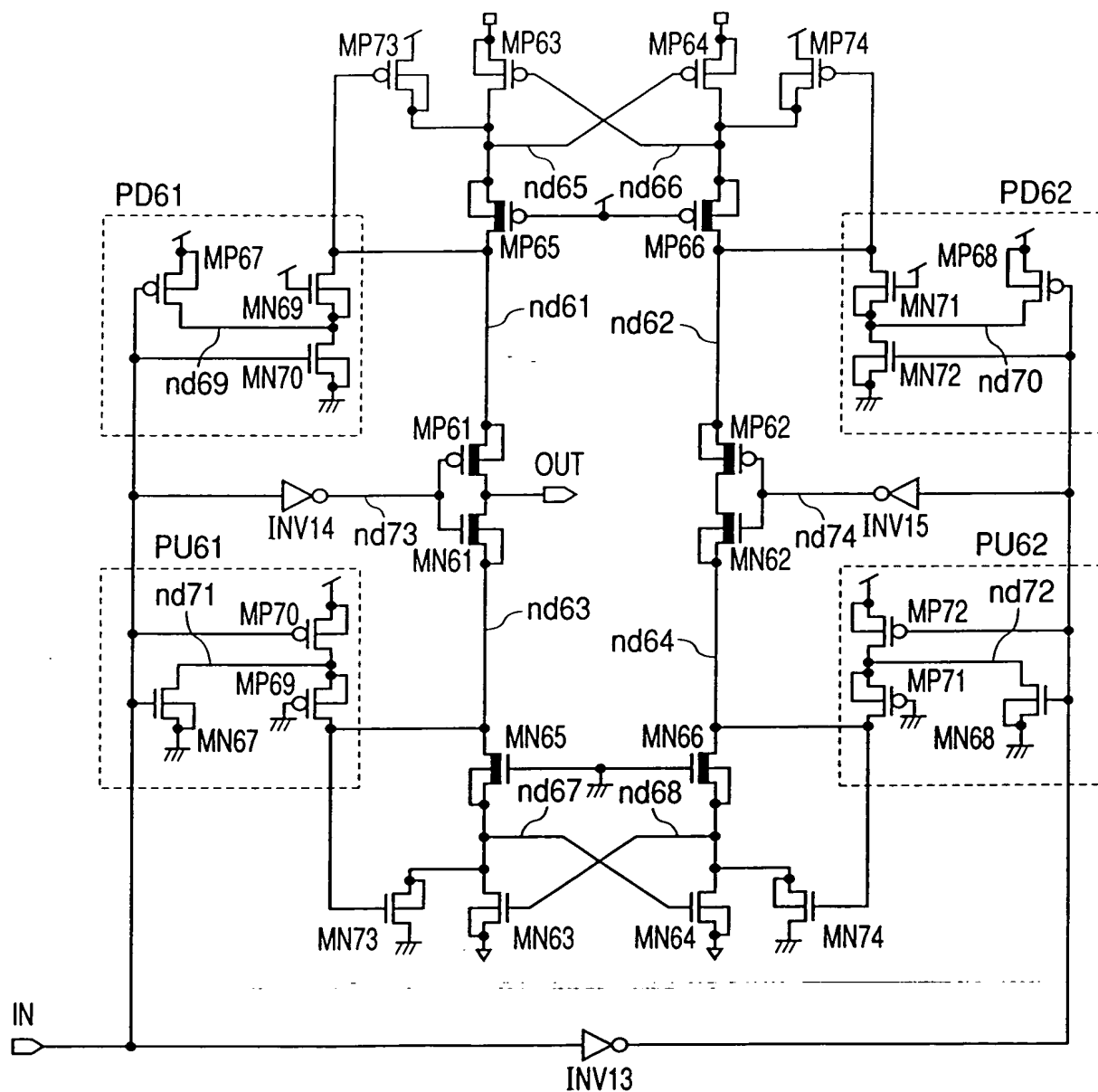
15 / 20

FIG. 15



16 / 20

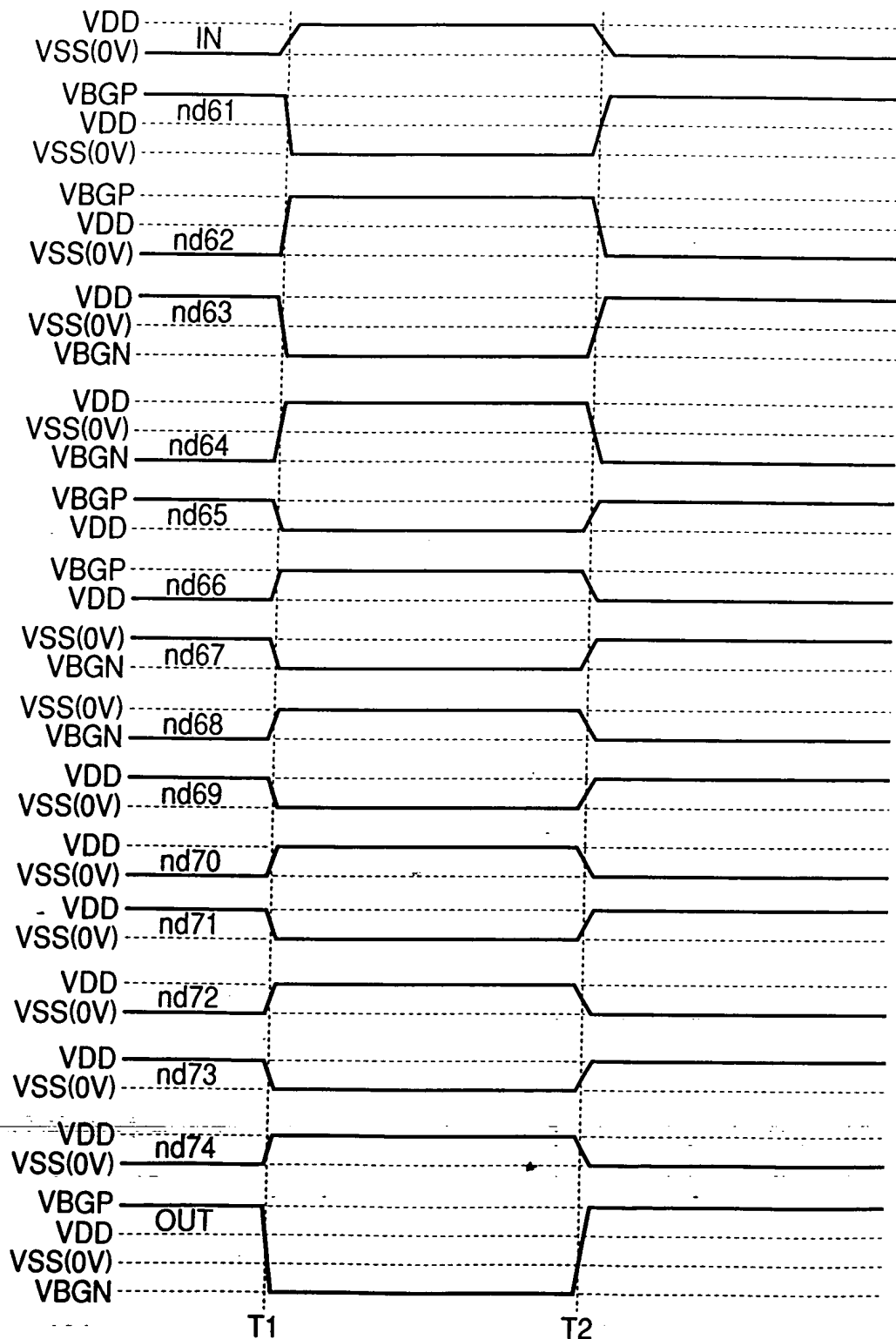
FIG. 16



VDD VSS VBGP VBGN
↑ ↓

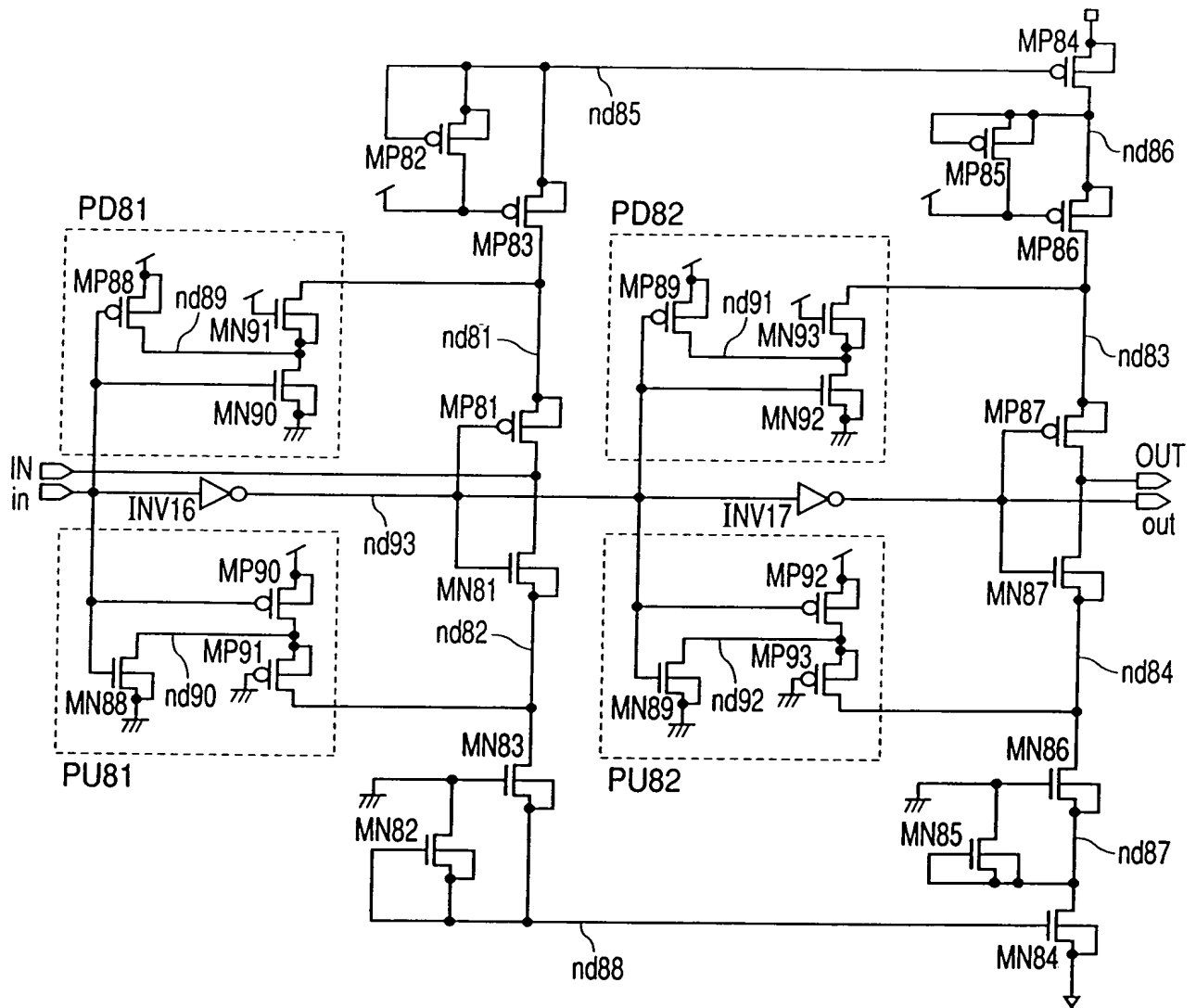
17 / 20

FIG. 17



18 / 20

FIG. 18



VDD VSS VBGP VBGN

↑ ↓ ↑ ↓

19 / 20

FIG. 19

